

IN THE CLAIMS

Please amend the claims as follows:

Claim 1 (Currently Amended): A drive method for an EL display panel, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements; and

a gate driver circuit which turns on and off the first switching elements for control;

wherein:

the gate driver circuit generates a plurality of stripe non-display areas on a display screen of the EL display panel by controlling turns on and off the first switching elements in an off-state two or more times during one frame period, [[and]]

the gate driver circuit moves the plurality of stripe non-display areas in a scanning direction of the gate driver circuit, and

an operation for retaining an image signal applied to each pixel is retained only once during the one frame period.

Claim 2 (Currently Amended): A drive method for an EL display panel, the EL display panel comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements; and

first switching elements placed in current paths of the EL elements; and

a gate driver circuit which selects pixel row of the EL display panel in sequence turns on and off the first switching elements for control;

~~wherein the gate driver circuit keeps the first switching elements off two horizontal scanning periods during one frame period, and~~

wherein a start pulse to be input into the gate driver circuit is controlled,

a plurality of stripe non-display areas on a display screen of the EL display panel are generated and the plurality of stripe non-display areas are moved in a scanning direction of the gate driver circuit; and

an operation for retaining an image signal applied to each pixel is retained executed only once during the one frame period.

Claim 3 (Cancelled)

Claim 4 (Previously Presented): The drive method for the EL display panel according to claim 1,

wherein the first switching elements are turned off periodically during one frame period.

Claim 5 (Currently Amended): An EL display panel, comprising:  
a source driver circuit which outputs an image signal;  
EL elements arranged in a matrix;  
driver transistors which supply current to be passed through the EL elements;  
first switching elements placed in current paths of the EL elements;  
second switching elements which constitute paths used to transmit the image signal to the driver transistors;  
a first gate driver circuit which turns on and off the first switching elements for control; and

a second gate driver circuit which turns on and off the second switching elements for control;

wherein:

each of the second switching elements has a plurality of transistor elements,  
each of the first switching elements and the each of the second switching elements are  
constituted so that the first and second switching elements are controlled independently of  
each other for turning on and off by the first and second gate driver circuits, and  
the first gate driver circuit controls turns-off the first switching elements in state of off  
a number of times during one frame period, and generates strip non-display areas on a display  
screen of the EL display panel and moves the plurality of strip non-display areas in a  
scanning direction of the first gate driver circuit  
~~an image signal applied to each pixel is retained only once during the one frame~~  
~~period.~~

Claim 6 (Previously Presented): The EL display panel according to claim 5, wherein the first and second gate driver circuits are formed in a same process as the driver transistors and the source driver circuit is made of a semiconductor chip.

Claim 7 (Currently Amended): An EL display panel according to claim 5,  
~~comprising:~~  
~~gate signal lines;~~  
~~source signal lines;~~  
~~a source driver circuit which outputs an image signal;~~  
~~a gate driver circuit;~~  
~~EL elements arranged in a matrix;~~

~~driver transistors which supply current to be passed through the EL elements; and second transistors which constitute paths used to transmit an image signal to the driver transistors;~~

~~wherein:~~

~~the gate signal lines are connected to the gate driver circuit,~~  
~~gate terminals of the second transistors are connected to the gate signal lines,~~  
~~source terminals of the second transistors are connected to the source signal lines, and~~  
wherein the second gate driver circuit selects a plurality of gate signal lines and supplies the image signal to the driver transistors of a plurality of pixel rows.

Claim 8 (Withdrawn): An EL display panel, comprising:  
a display area in which pixels having an EL element are arranged in a matrix;  
a dummy pixel row formed outside the display area,  
a source driver circuit which applies an image signal to source signal lines connected to pixels in the display area and to pixels in the dummy pixel row; and  
a gate driver circuit which applies a turn-on voltage or turn-off voltage to gate signal lines connected to pixels in the display area and to pixels in the dummy pixel row;  
wherein the dummy pixel row either does not emit light or emits light not visible to the eye.

Claim 9 (Cancelled).

Claim 10 (Currently Amended): The EL display panel according to claim [[7]] 5,  
wherein the gate driver circuit is constructed of p-channel transistors.

Claim 11 (Withdrawn): An EL display panel, comprising:

EL elements arranged in a matrix;

driver transistors which supply current to be passed through the EL elements;

first switching elements placed in current paths of the EL elements;

a gate driver circuit which turns on and off the first switching elements for control;

and

a source driver circuit which supplies programming current to the driver transistors,

wherein:

the gate driver circuit keeps the first switching elements off for two horizontal scanning periods during one frame period, and

an image signal applied to each pixel is retained only once during the one frame period.

Claim 12 (Withdrawn): A drive method for an EL display panel in which pixels having respective EL elements are arranged in a matrix, comprising the steps of:

supplying EL elements with a current which makes the EL elements emit light brighter than a predetermined brightness; and

making the EL elements emit light for a period equal to  $1/N$  of one frame period or one field period ( $N$  is a real number larger than 1).

Claim 13 (Withdrawn): The drive method for the EL display panel according to claim 12, wherein the period equal to  $1/N$  of a frame is divided into a plurality of periods.

Claim 14 (Withdrawn): A drive method for an EL display panel in which pixels having respective EL elements are arranged in a matrix, comprising the steps of:

making the EL elements emit light brighter than a predetermined brightness;

displaying a display area equal to  $1/N$  ( $N$  is a real number larger than 1) of an entire screen; and

shifting the display area of  $1/N$  of the entire screen in sequence to display the entire screen.

Claim 15 (Previously Presented): An EL display apparatus comprising:

the EL display panel according to claim 5, and a receiver.

Claim 16 (New): The drive method for the EL display panel according to claim 1, wherein the driver transistors are P-channel resistors.

Claim 17 (New): The drive method for the EL display panel according to claim 2, wherein the driver transistors are P-channel resistors.

Claim 18 (New): The drive method for the EL display panel according to claim 1, wherein brightness of the display screen is varied or controlled by varying proportion of the non-display area to a display area of the display screen.

Claim 19 (New): The drive method for the EL display panel according to claim 2, wherein brightness of the display screen is varied or controlled by varying proportion of the non-display area to a display area of the display screen.

Claim 20 (New): The EL display panel according to claim 5, wherein the driver transistors are P-channel transistors.

Claim 21 (New): The EL display panel according to claim 5, wherein the first gate driver circuit is formed with P-channel transistors.